

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
M.TECH. (EMBEDDED SYSTEMS) 2009-10
COURSE STRUCTURE AND SYLLABUS

I YEAR - I Semester

Code	Group	Subject	L	P	Credits
		Microcontrollers for Embedded System Design	3	0	3
		Embedded Real Time Operating Systems	3	0	3
		VLSI Technology & Design	3	0	3
		Advanced Computer Networks	3	0	3
	Elective -I	Advanced Computer Architecture	3	0	3
		Wireless LANs & PANs			
		Advanced Digital Signal processing			
	Elective -II	Digital System Design	3	0	3
		Neural Networks & Applications			
		Advanced Operating Systems			
	Lab	Embedded Systems Lab - I	0	3	2
		Seminar	-	-	2
		Total Credits (6 Theory + 1 Lab.)			22

I YEAR - II Semester

Code	Group	Subject	L	P	Credits
		Hardware Software Co-Design	3	0	3
		Digital Signal Processors and Architectures	3	0	3
		System Modeling and Simulation	3	0	3
		CPLD & FPGA Architectures and Applications	3	0	3
	Elective - III	Low Power VLSI Design	3	0	3
		Robotics			
		Ad-hoc Wireless & Sensor Networks			
	Elective – IV	Image & Video Processing	3	0	3
		System On Chip Architecture			
		Network Security & Cryptography			
	Lab	Embedded Systems Lab-II Using PSoC	0	3	2
		Seminar	-	-	2
		Total Credits (6 Theory + 1 Lab.)			22

II YEAR - I Semester

Code	Group	Subject	L	P	Credits
		Comprehensive Viva	-	-	2
		Project Seminar	0	3	2
		Project work	-	-	18
		Total Credits			22

II YEAR - II Semester

Code	Group	Subject	L	P	Credits
		Project work and Seminar	-	-	22
		Total Credits			22