

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. TECH. ELECTRONICS AND COMMUNICATION ENGINEERING (2009-10)

I YEAR

COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	English	2	-	4
	Mathematics – I	3	1	6
	Mathematical Methods	3	1	6
	Engineering Physics	2	1	4
	Engineering Chemistry	2	-	4
	Computer Programming & Data Structures	3*	-	6
	Engineering Drawing	2	3	4
	Computer Programming Lab.	-	3	4
	Engineering Physics / Engineering Chemistry Lab.	-	3	4
	English Language Communication Skills Lab.	-	3	4
	IT Workshop / Engineering Workshop	-	3	4
	Total	17	18	50

II YEAR I SEMESTER

COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	Mathematics – III	3	1	3
	Probability Theory & Stochastic Processes	3	1	3
	Environmental Studies	3	1	3
	Electric Circuits	4	1	4
	Electronic Devices & Circuits	4	-	4
	Signals & Systems	4	1	4
	Electronic Devices & Circuits Lab.	-	3	2
	Basic Simulation Lab.	-	3	2
	Total	21	11	25

II YEAR II SEMESTER

COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	Principles of Electrical Engineering	3	1	3
	Electronic Circuit Analysis	4	-	4
	Pulse & Digital Circuits	4	1	4
	Switching Theory & Logic Design	4	1	4
	Electromagnetic Theory & Transmission Lines	4	1	4
	Electrical Engineering Lab.	-	3	2
	Electronic Circuit Analysis Lab.	-	3	2
	Pulse & Digital Circuits Lab.	-	3	2
	Total	19	13	25

III YEAR I SEMESTER

COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	Control Systems.	4	-	4
	Computer Organization	4	1	4
	Antennas & Wave Propagation	3	1	3
	Electronic Measurements & Instrumentation	4	1	4
	Analog Communications	3	1	3
	IC Applications	3	1	3
	Analog Communications Lab.	-	3	2
	IC Applications Lab.	-	3	2
	Total	21	11	25

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III YEAR II SEMESTER COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	Managerial Economics & Financial Analysis	4	-	4
	OPEN ELECTIVE Operating Systems Object Oriented Programming Nano technology	4	1	4
	Digital Communications	3	1	3
	Microprocessors & Microcontrollers	4	1	4
	Digital Signal Processing	4	1	4
	Microprocessors & Microcontrollers Lab.	-	3	2
	Digital Signal Processing Lab.	-	3	2
	Adv. English Language Communication Skills Lab.	-	3	2
	Total	19	13	25

IV YEAR I SEMESTER COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	Management Science	3	1	3
	VLSI Design	4	-	4
	Microwave Engineering	3	1	3
	Computer Networks	4	1	4
	ELECTIVE – I EMI/EMC DSP Processors & Architectures Telecommunication Switching Systems Digital Image Processing	3	1	3
	ELECTIVE – II Optical Communications Embedded Systems Television Engineering Multimedia and Signal coding	4	1	4
	e-CAD & VLSI Lab.	-	3	2
	Microwave Engg. & Digital Commns. Lab.	-	3	2
	Total	21	11	25

IV YEAR II SEMESTER COURSE STRUCTURE

Code	Subject	L	T/P/D	C
	ELECTIVE-III Cellular & Mobile Communications Satellite Communications Biomedical Instrumentation Artificial Neural Networks	3	1	3
	ELECTIVE – IV Internetworking Radar Systems Spread Spectrum Communications Network Security	3	1	3
	ELECTIVE –V RF Circuit Design Wireless Communications & Networks Digital Design through Verilog HDL Pattern Recognition	3	1	3
	Industry Oriented Mini Project	-	-	2
	Seminar	-	6	2
	Major Project	-	15	10
	Comprehensive Viva	-	-	2
	Total	9	24	25